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DEC 08 2003  
PATENT & TRADEMARK OFFICE

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Confirmation No.: 6076

Hiroyoshi TOMITA

Group Art Unit: 2816

Serial No.: 10/078,340

Examiner: Minh Nguyen

Filed: February 21, 2003

Atty. Docket No.: 108397-00067

For: SEMICONDUCTOR INTEGRATED CIRCUIT, METHOD OF CONTROLLING  
THE SAME, AND VARIABLE DELAY CIRCUIT

**AMENDMENT UNDER 37 CFR § 1.111**

Director for the U.S. PTO  
P.O. Box 1450  
Alexandria, VA 22313-1450

December 8, 2003

Sir:

In reply to the outstanding Office Action dated September 11, 2003, please  
consider the following:

RECEIVED  
DEC 11 2003  
TECHNOLOGY CENTER 2800